Amendments to the Specification:

Please replace paragraph 0045 with the following replacement paragraph:

An update write scenario to the same track can be provided as follows. Steps 1 and 2 are the same as steps H1 and H2 as discussed in connection with Fig. 3. In Step C3, the cache will now allocate NVS segments 6 and 7. The cache will allocate track buffer 1, and move segments 6 and 7 from the cache control block to "B" segments, and move segments 2 and 3 to the cache control block. Cache steps C4 and C5 use track buffer 1. Host adapter step H5 uses buffer 1 and segments 2 and 3. NVS step N8 changes as follows:

1. If the entire track was written again, then the NVS control block for the track is updated to reflect that segments 2 and 3 have the data.

2. If either segment is completely written, then the NVS control block for the track is updated to reflect the new segment.

3. If a segment is partially written, then the customer data in the segment with the least amount of ~~date~~ data is copied to the segment with the most amount of data.

All other steps remain the same.

2